

Mercury Computer Systems Launches High Performance Products for Defense Applications Based on the 2nd Generation Intel(R) Core(TM) Processor Family

Rugged, OpenVPX Building Blocks Based on Intel ^(R) Core^(TM) Processors Deliver up to Four Times the Raw Performance of Previous Generations for Deployment in Harsh Military and Aerospace Environments

CHELMSFORD, Mass., Jan 05, 2011 (BUSINESS WIRE) --

Mercury Computer Systems, Inc. (NASDAQ: MRCY, <u>www.mc.com</u>), a trusted ISR subsystems provider, announced the

Ensemble[™] Series 6U OpenVPX[™] LDS6521 and the 3U OpenVPX SBC3510 modules based on the 2nd generation Intel[®]

Core processor family.Mercury's new modules enable best-of-breed subsystem application performance for extremely demanding ISR, defense, and aerospace applications.

Sensors and antennas are delivering images and data with incredible detail, providing overwhelming volumes of high-resolution digital data on both manned and unmanned platforms, often demanding a 5-10X increase in processing capability. Mercury's

6U OpenVPX LDS6521 module utilizes the Intel [®] Core[™] i7-2715QE processor, significantly narrowing this gap. The Intel Core i7-2715QE processor doubles the number of cores available on a single device over the previous generation and now supports

four cores. Each of these cores includes the new Intel[®] Advanced Vector Extensions (Intel[®] AVX) instruction set with only a minimal increase in power consumption. Together, these new features give Mercury's LDS6521 a 4X improvement in peak floating point computations per slot over previous module designs while maintaining an appropriate SWaP-optimized power profile.

Additionally, ISR subsystems utilizing multiple Mercury LDS6521 modules benefit from <u>POET</u>, Mercury's previously announced Protocol Offload Engine Technology, enabling fast and low latency data communications between the new Intel Core processor-based modules as system size is scaled up. The combination of 2nd generation Intel Core processor performance and POET interconnect technology makes Mercury's LDS6521 an ideal choice for sensor-driven ISR subsystems.

In addition to demand for higher performance, defense applications such as Command and Control (C2) for ground mobile vehicles, as well as tactical UAV applications, such as EO/IR and EW, are optimizing for smaller size, lower power and lower

cost. Mercury's 3U OpenVPX SBC3510 meets these requirements, incorporating the dual core Intel [®] Core [™] i7-2655LE processor, an industry standard XMC daughter card site and diverse I/O capabilities, providing traditional SBC functionality in a small 3U package. Like the four core device, the two core version also benefits from the new Intel AVX instruction set, but with significantly lower power requirements and high MTBF, making it ideal for 3U OpenVPX-based defense applications.

"Mercuryis committed to keep pace with the Intel[®] Core[™] processor roadmap and to deliver superior products and performance that address the most difficult ISR processing challenges," said Steve Patterson, Vice President of Defense Product Management at Mercury Computer Systems, Inc. "These new embedded computing products exemplify Mercury's commitment to deliver best-of-breed solutions for demanding radar, EW, and EO/IR applications incorporating the latest Intel processors," Patterson added.

"The 2nd generation Intel[®] Core[™] processor family is ideal for embedded computing products with its increase in performance stemming from the new Intel[®] Advanced Vector Extensions instruction set while maintaining the Intel instruction set architecture (ISA)," said Matt Langman, director of product marketing, Embedded Computing Division, Intel. "The performance and integrated features of our new processors deliver unprecedented capabilities for even the most challenging applications and harshest environments."

Mercury's LDS6521 and SBC3510 are available in air-cooled and conduction-cooled rugged versions. The LDS6521 supports

two XMC mezzanine sites, an Intel Core i7-2715QE processor, and support via POET for serial RapidIO[®] Gen 1 and Gen 2 or low latency 10 Gigabit Ethernet to the backplane/dataplane. Typical ISR subsystems combine this module with 6U OpenVPX subsystem components, such as Mercury's HDS6600 Intel-based high-density server and the SFM6100 multi-function switch module. The SBC3510 combines a single XMC mezzanine site and a dual-core Intel Core i7-2655LE processor, and provides SBC style I/O. On larger 3U systems, these modules are typically configured with Mercury's FCN3110 FPGA compute module

and SFM3010 integrated switch for compute-intensive 3U based ISR subsystems.

For more information on the Ensemble 6U LDS6521 module, visit <u>mc.com/lds6521</u>. For more information on the Ensemble 3U SBC3510 module, visit <u>mc.com/sbc3510</u>, or contact Mercury at (866) 627-6951 or <u>info@mc.com</u>.

Mercury Computer Systems, Inc. - Where Challenges Drive Innovation $^{\circledast}$

Mercury Computer Systems (<u>www.mc.com</u>, NASDAQ: MRCY) is a best of breed provider of open, application-ready, multi-INT subsystems for the ISR market. With 25+ years' experience in embedded computing, superior domain expertise in radar, EW, EO/IR, C4I, and sonar applications, and more than 300 successful program deployments including Aegis, Global Hawk, and Predator, Mercury's Services and Systems Integration team leads the industry in partnering with defense and commercial customers to design and integrate system-level solutions that minimize program risk, maximize application portability, and accelerate customers' time to market.

Mercury is based in Chelmsford, Massachusetts, and serves customers worldwide through a broad network of direct sales offices, subsidiaries, and distributors.

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This press release contains certain forward-looking statements, as that term is defined in the Private Securities Litigation Reform Act of 1995, including those relating to the products and services described herein. You can identify these statements by the use of the words "may," "will," "should," "plans," "expects," "anticipates," "continue," "estimate," "project," "intend," and similar expressions. These forward-looking statements involve risks and uncertainties that could cause actual results to differ materially from those projected or anticipated. Such risks and uncertainties include, but are not limited to, general economic and business conditions, including unforeseen weakness in the Company's markets, effects of continued geopolitical unrest and regional conflicts, competition, changes in technology and methods of marketing, delays in completing engineering and manufacturing programs, changes in customer order patterns, changes in product mix, continued success in technological advances and delivering technological innovations, continued funding of defense programs, the timing of such funding, changes in the U.S. Government's interpretation of federal procurement rules and regulations, market acceptance of the Company's products, shortages in components, production delays due to performance quality issues with outsourced components, inability to fully realize the expected benefits from acquisitions and divestitures or delays in realizing such benefits, challenges in integrating acquired businesses and achieving anticipated synergies, changes to export regulations, increases in tax rates, changes to generally accepted accounting principles, difficulties in retaining key employees and customers, unanticipated costs under fixed-price service and system integration engagements, and various other factors beyond our control. These risks and uncertainties also include such additional risk factors as are discussed in the Company's filings with the U.S. Securities and Exchange Commission, including its Annual Report on Form 10-K for the fiscal year ended June 30, 2010. The Company cautions readers not to place undue reliance upon any such forward-looking statements, which speak only as of the date made. The Company undertakes no obligation to update any forward-looking statement to reflect events or circumstances after the date on which such statement is made.

SOURCE: Mercury Computer Systems, Inc.

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